







# Report

# Inaugural Ceremony Industry based Summer Internship (6 week)

on

## RTL to GDS-II: From Silicon to Design

16th June 2025 to 26th June 2025

Venue: GTU – School of Engineering and Technology, Academic Block 5, GTU Campus, Chandkheda, Ahmedabad

Coordinator: Prof. Gautam D. Makwana Co-coordinator: Prof. Parth M. Chauhan

The inaugural ceremony of the six-week Industry-Based Summer Internship on "RTL to GDS-II: From Design to Silicon" was held on 16th June 2025, from 11:00 AM to 11:45 AM, at the GTU School of Engineering and Technology, Chandkheda Campus.

This internship program is being conducted by EInfochips – An Arrow Company, under the Chips to Start-ups Program of the Ministry of Electronics and Information Technology (MeitY), with valuable support from the ICICI Foundation. The organizing institutions include the GTU School of Engineering & Technology, the GTU Research and Development Cell, and EInfochips.

A total of 67 students, all from the Electronics and Communication Engineering branch and currently in their 6th semester, have been selected for this internship. These students come from various GTU-affiliated engineering colleges across Gujarat, making the program a truly state wide initiative aimed at empowering young minds in the semiconductor domain.

The institutional representation of the selected students is as follows:

Name of Institute	Number of students
Vishwakarma Government Engineering College, Ahmedabad	26
L.D. College of Engineering, Ahmedabad	18
Government Engineering College, Gandhinagar	11
Government Engineering College, Rajkot	4
Dr. S and S.S. Ghandhy Government Engineering College, Surat	4
Government Engineering College, Dahod	3
SAL Institute of Technology & Engineering Research, Ahmedabad	1

These participants will undergo rigorous, hands-on training in industry-standard VLSI workflows—gaining practical insights into the entire ASIC design cycle from RTL coding to GDS-II handoff.

#### **Presence of Dignitaries:**

The ceremony was graced by the esteemed presence of:

Dr. Pankajrai Patel, In-charge Hon'ble Vice Chancellor, Gujarat Technological University (GTU)

Dr. R.A. Thakker, Director, Research and Development Cell, GTU

Dr. Nilesh Ranpura, Guest of Honour, Director, EInfochips-An Arrow company

Prof. J.A. Amin, Head of Institute, GTU School of Engineering and Technology

### **Proceedings of the Ceremony:**



Saraswati Vandana and Lamp lighting

- The ceremony commenced on a traditional and spiritual note with the recitation of *Saraswati Vandana*, invoking the blessings of the goddess of knowledge and wisdom.
- This was followed by the ceremonial lamp lighting by esteemed dignitaries, symbolizing the triumph of knowledge over ignorance and the beginning of an enlightening academic journey.
- The dignified start set a reflective and aspirational tone for the six-week internship ahead.



Welcome Address by Prof. J.A. Amin (HOI, GTU-School of Engineering & Technology)

- Prof. Amin extended a heartfelt welcome to the dignitaries, speakers, industry mentors, and students, acknowledging the collaborative spirit behind the initiative.
- He underscored GTU's vision of providing experiential learning through direct exposure to advanced VLSI design flows and industry-grade tools.
- Highlighting the relevance of the internship, he urged students to approach it with commitment, curiosity, and a drive for excellence.



Address by Dr. Pankajraj Patel (In-charge

- Hon'ble Vice Chancellor, GTU)

Keynote Address by Dr. Nilesh Ranpura (EInfochips – An Arrow Company)



Vote of Thanks by Dr. R.A. Thakker (Director, RDC, GTU)

- Dr. Pankajrai Patel articulated the broader strategic goal of GTU to create a pipeline industry-ready talent for semiconductor ecosystem.
- He lauded the collaborative effort between GTU, EInfochips, MeitY, and the ICICI Foundation in nurturing future chip designers.
- Encouraging the participants, he emphasized that initiatives like this internship are not just training programs but platforms for transformation and discovery.
- Dr. Nilesh Ranpura delivered a compelling address on the real-world importance of the RTL to GDS-II design flow, placing it within the broader context of global semiconductor innovation.
- He offered technical insights into the design challenges faced by industry and how this internship bridges critical skill gaps for aspiring engineers.
- His message resonated with inspiration, urging students to think beyond coursework and aspire to be innovators and contributors to India's VLSI landscape.
- Dr. Rajesh Thakker expressed deep appreciation to all supporting dignitaries, and faculty organizations, members who made the event possible.
- He acknowledged the pivotal role of partnerships industry in translating academic learning into employable skills.
- Concluding on a motivational note, he encouraged students to fully engage in the internship and seize every opportunity for learning and growth.



Conclusion of ceremony with National Anthem

- The formal function concluded with the collective rendition of the National Anthem, instilling unity, discipline, and a sense of national purpose.
- Prof. Gautam Makwana then addressed the participants informally, offering a comprehensive orientation on the internship structure, modules, and deliverables.
- He motivated students to take ownership of their learning journey, be inquisitive, maintain discipline, and make the most of the rare opportunity to work closely with industry experts.

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