


DR. S. D. PANCHAL
PROFESSOR



Branch	Computer Engineering				
Date of Joining the Institution	05/05/2020				
Qualification with Class & Grade	UG	PG		PHD	
	FIRST CLASS	DISTINCTION		Ph.D.	
Total Experience in Years	Teaching	Industries		Research	
	23	00		05	
Research Profile	Research Area	Journal Publication		No. of Books/Book Chapter/Conference Publication	
	Computer Networking & Security	15		08	
	H-Index	No. of Ph.D. Guided Students		No. of Ph.D. Student (Guiding)	
	04	00		06	
	No. of M.E. Student Guided	Citation	No. of Awards/Recognition	Patents	
	18	36	03	00	
Professional Membership	<ul style="list-style-type: none">• IEEE Senior Member• Life time (IQAC) Membership				
Consultancy Activities	-				
Project Funding Details		Name of Project	Amount	Funding Agency	Status
	Internal	A novel assured data deletion scheme with public verifiability for cloud storage using blockchain (CO-PI)	1,00,000/-	GTU – Seed Money Scheme	completed
	External	Investigation, analysis of multiband Array Antenna for 5G Mobile Handset in IoT Application (CO-PI)	20.77 Lacs	GUJCOST, G'nagar	Ongoing
Committee members in various bodies	<ul style="list-style-type: none">• Member, IT Committee, GTU.• Member Secretary responsible educational Board ,GTU.• Member, Board of Governance, GTU.• Member of Academic Council, GTU.• Head IQAC, GTU.• Chairperson BoS PG Engineering, GTU• Chairperson BoS UG. Engineering branches, Engineering GTU• Chairperson, Doctoral Research Committee, PhD., GTU• Hon. Director of Research & Comm. Cell, GTU.• Member, Project monitoring committee : GTU-AICTE book translation• Member, Research program and policy development, GTU• Member, Academic Council, BVM vidhyanagar• Chief-Member, GTU-AICTE IDEA Lab• Coordinator, Center of Excellence in Blockchain Technology				